

Appl. No. 10/615,918
Amdt. Dated 02/10/2005
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Appendix 4

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for IDS filed on 10/17/2003

Application No: **10/615,618** Filing Date: **07/08/2003**

BSTZ Docket #: **042390.P7098C**

Date Mailed: **10/12/2003** Docket Due Date(s): **10/08/2003**

Atty/Sec: **WEA/sm**

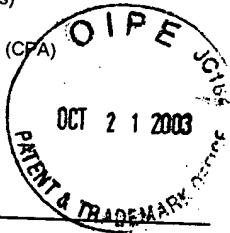
Client: **INTEL CORPORATION**

Title **METHOD AND APPARATUS FOR MULTI-THREAD PIPELINED INSTRUCTION DECODER**

Inventor(s) **Douglas Deleagan, Hadley**

The following has been received in the U.S. Patent & Trademark Office on the date stamped hereon:

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| <input type="checkbox"/> Amendment: (___ pgs) | <input checked="" type="checkbox"/> Information Disclosure Statement & PTO/SB/08 (___ pgs) |
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| <input type="checkbox"/> Application:
(___ pages w/ cover & abstract) | <input type="checkbox"/> Nonpublication Request Under 35 U.S.C. 122(b)(2)(B)(i) |
| <input type="checkbox"/> Application Amended to Reflect Claim of Priority | <input type="checkbox"/> Notice of Appeal |
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Assignee: Intel Corporation